

Applicant : Shunpei Yamazaki et al. Art Unit : Unknown
Serial No. : 09/919,832 Examiner : Unknown
Filed : August 2, 2001
Title : PORTABLE INFORMATION APPARATUS AND METHOD OF DRIVING
THE SAME

Attention: Official Draftsman
Commissioner for Patents
Washington, D.C. 20231

TRANSMITTAL OF OMITTED DRAWING FIGURES

Applicants respectfully request entry of the attached drawing figures (Figs 35, 36 and 37), which were erroneously omitted from the original application, as filed.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: August 20, 2001


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Attorney's Docket No. 12732-063001	Express Mail Label No.	Mailing Date August 20, 2001	For PTO Use Only <i>Do Not Mark in This Area</i>
Application No. 09/919,832	Filing Date August 2, 2001	Attorney/Secretary Init JFH/aks	
Title of the Invention PORTABLE INFORMATION APPARATUS AND METHOD OF DRIVING THE SAME			
Applicant Shunpei Yamazaki et al.			
Enclosures <ul style="list-style-type: none">• Supplemental Information Disclosure Statement (1 page)• Form PTO-1449 (in duplicate, 2 pages)• Documents listed on the Form PTO-1449 (3 documents)• Transmittal of Omitted Drawing Figures (1 page)• Drawings (Figs. 35, 36 and 37, 3 sheets)			

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The diagram illustrates a 3-bit digital-to-analog converter (D/A) circuit. It features three input lines, 101, 102, and 103, which are connected to three comparators, 105, 106, and 107, respectively. Each comparator has a feedback input and a control input. The control inputs are connected to lines 121, 122, and 123, which are outputs of a digital-to-analog converter (D/A) block, 111. The feedback inputs are connected to lines 124, 125, and 126, which are also outputs of the D/A block 111. The D/A block 111 is connected to a feedback loop consisting of a capacitor, 115, and a switch, 114, which is connected to ground. The output of the D/A block 111 is also connected to a feedback loop consisting of a capacitor, 115, and a switch, 114, which is connected to ground. The output of the D/A block 111 is also connected to a feedback loop consisting of a capacitor, 115, and a switch, 114, which is connected to ground.

Fig. 35

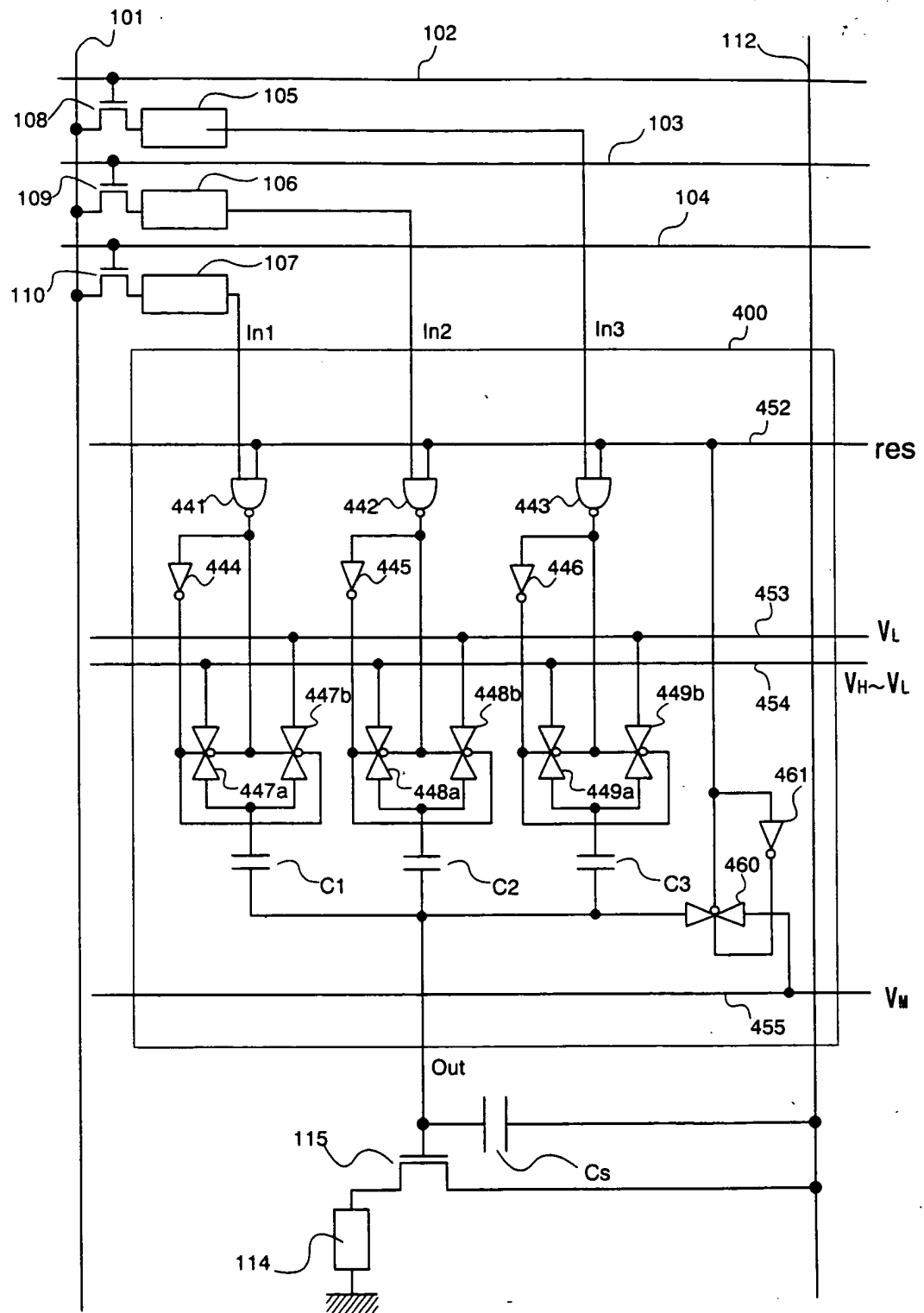


Fig. 36

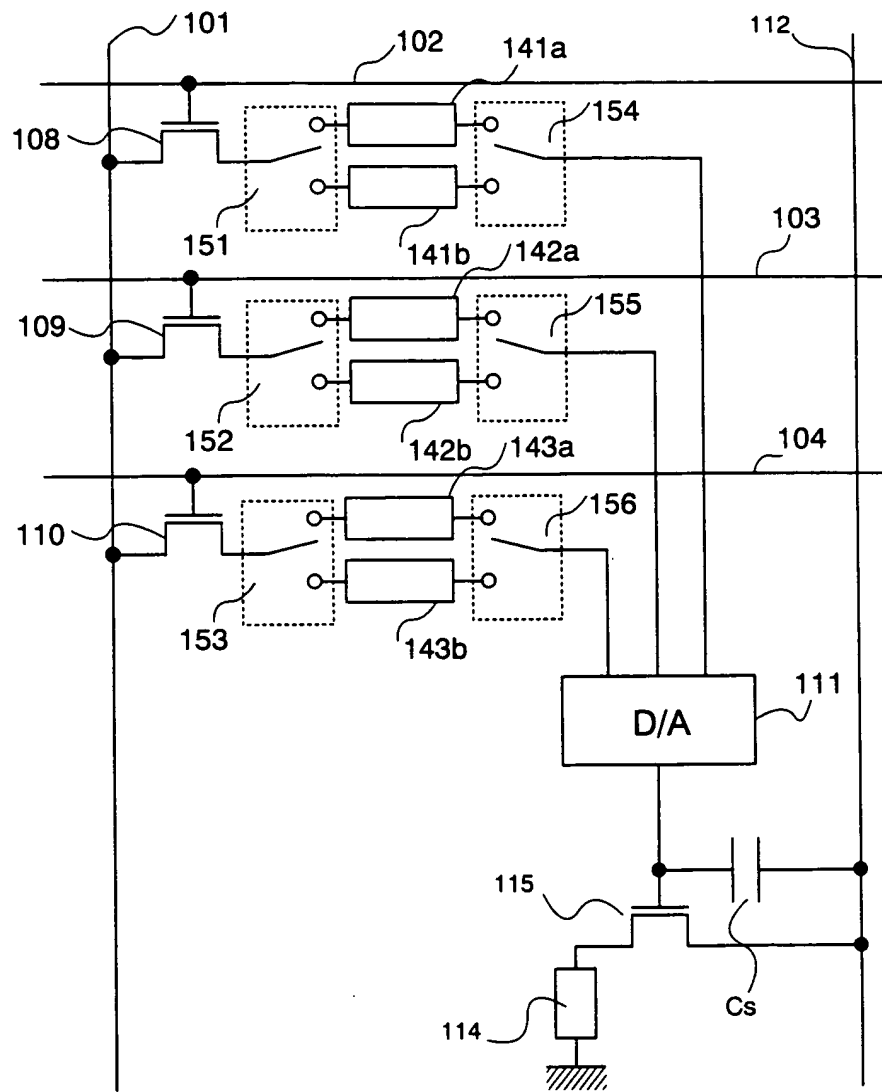


Fig. 37